

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Michael W. Morrow

Serial No.: 10/661,079

Filed: September 12, 2003

For: Switching Processor Threads During
Long Latencies

§ Group Art Unit: 2183

§

§

§

§

§

§

§

§

§

Examiner: Unknown

Atty. Dkt. No.: ITL.1028US (P16764)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of the non-U. S. Patent references are enclosed.

This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account 20-1504.

Respectfully submitted,

Date: December 31, 2003

Mark J. Rozman
Registration No. 42,117
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Suite 100
Houston, Texas 77024
(512) 418-9944 [Phone]
(713) 468-8883 [Fax]



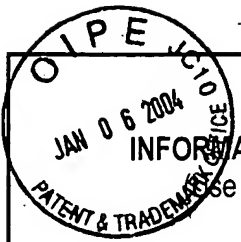
21906

PATENT-TRADEMARK OFFICE

Date of Deposit: December 31, 2003

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Jennifer Juarez



INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.

ITL1028US (P16764)

SERIAL NO.

10/661,079

APPLICANT(S):

MICHAEL W. MORROW

FILING DATE:

September 12, 2003

GROUP ART UNIT:

2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.	6,463,522	10/08/2002	AKKARY	712	216	
	B.	6,016,542	12/18/2000	GOTTLIEB ET AL.	712	225	
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J.							
	K.							
	L.							
	M.							
	N.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	O.	Marr, Deborah T., et al. "Hyper-Threading Technology Architecture and Microarchitecture" http://developer.intel.com/sites/corporate/tradmarx.htm . Intel Corporation, February 2002.
	P.	"Introduction to Hyper-Threading Technology" http://www.intel.com/technology/hyperthread/download/25000802.pdf . Intel Corporation, 2001.
	Q.	"Intel® XScale™ Microarchitecture for the PXA255 Processor-User's Manual" Table of Contents, and Appendix A. http://www.intel.com/design/pca/applicationsprocessors/manuals/27879601.pdf . Intel Corporation, March 2003.
	R.	
	S.	
	T.	
	U.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.